



Docket No.: 1687.1004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Koichiro ISHIBASHI et al.

Serial No. 10/618,710

Group Art Unit: 2818

Confirmation No. 4927

Filed: July 15, 2003

Examiner: Tu Tu V. Ho

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING BODY BIASING  
CIRCUIT FOR GENERATING FORWARD WELL BIAS VOLTAGE OF SUITABLE  
LEVEL BY USING SIMPLE CIRCUITRY

**AMENDMENT**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed June 10, 2004, and having a period for response set to expire on October 10, 2004.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.